Sheet 1 of 16

Title: Rounding Operations in Computer

Processo

Inventors: Frans W. Sijstermans and Jos van Eijndhoven

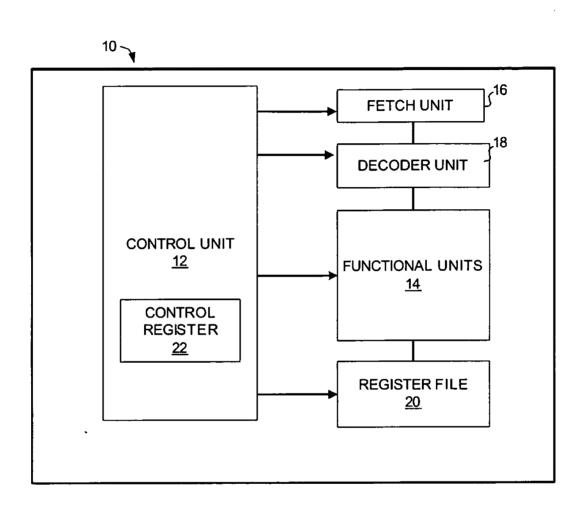


Fig. 1

Sheet 2 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans and Jos van Eijndhoven

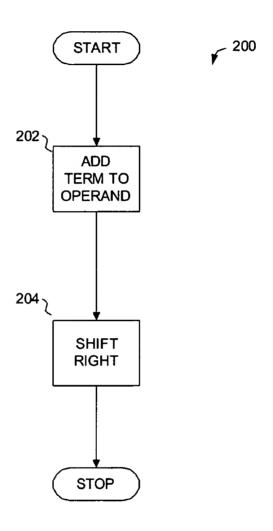


Fig. 2

Sheet 3 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans and Jos van Eijndhoven

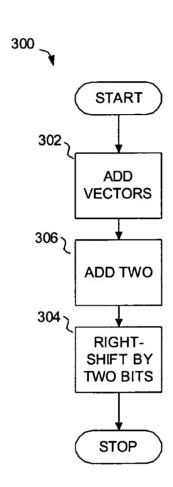


Fig. 3

Sheet 4 of 16

Title: Rounding Operations in Computer

Processo

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven Atty. Docket No.: 22300-05725

400

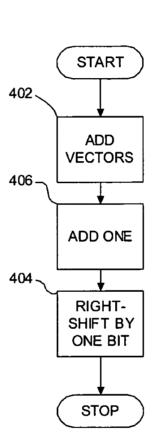


Fig. 4

Sheet 5 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans and Jos van Eijndhoven Atty. Docket No.: 22300-05725

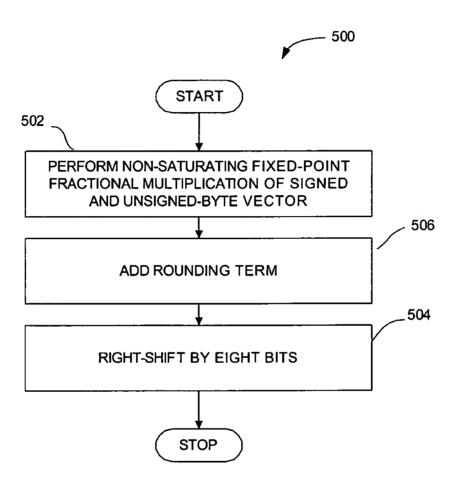


Fig. 5

Sheet 6 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

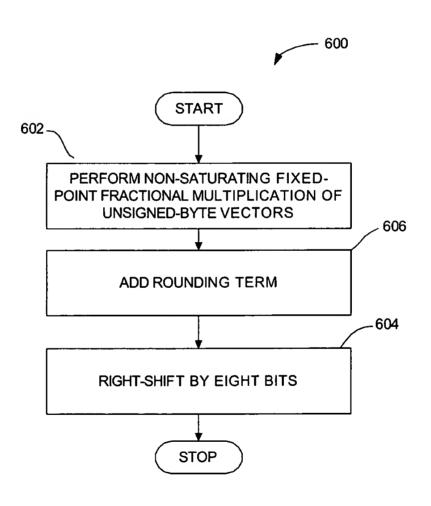


Fig. 6

Sheet 7 of 16

Title: Rounding Operations in Computer.

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

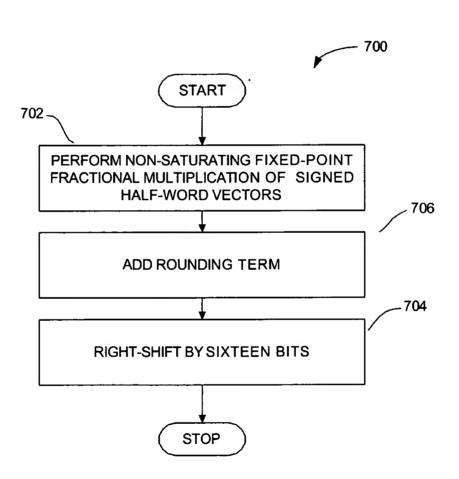


Fig. 7

Sheet 8 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans and Jos van Eijndhoven

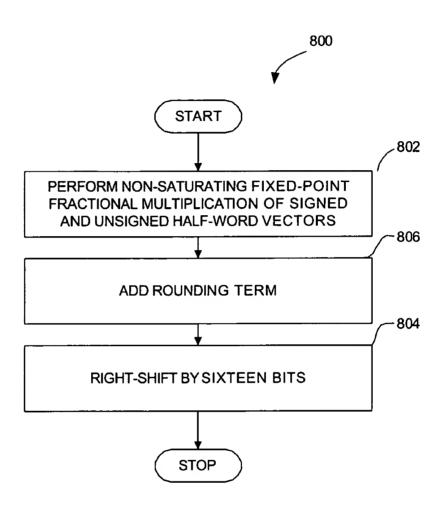


Fig. 8

Sheet 9 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans and Jos van Eijndhoven Atty. Docket No.: 22300-05725

PERFORM NON-SATURATING FIXED-POINT FRACTIONAL MULTIPLICATION OF UNSIGNED HALF-WORD VECTORS

906

ADD ROUNDING TERM

904

RIGHT-SHIFT BY SIXTEEN BITS

Fig. 9

STOP

Sheet 10 of 16

Title: Rounding Operations in Computer

Processo

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

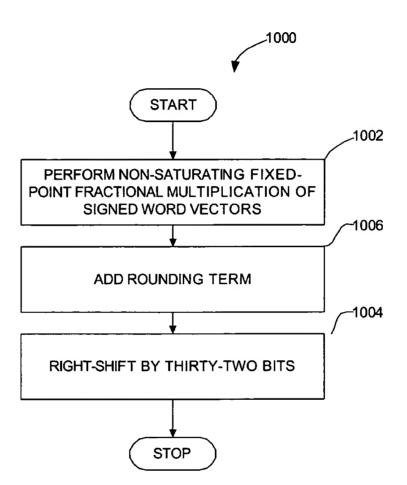


Fig. 10

Sheet 11 of 16

Title: Rounding Operations in Computer

Processo

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

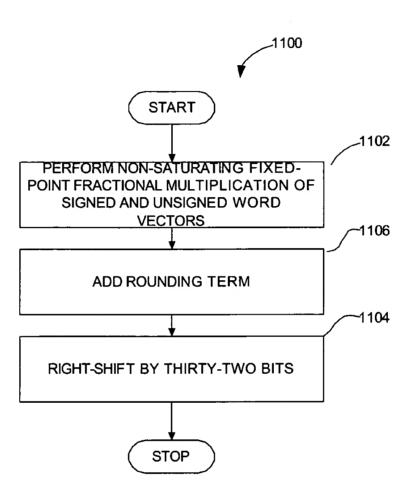


Fig. 11

Sheet 12 of 16

Title: Rounding Operations in Computer

Processo

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

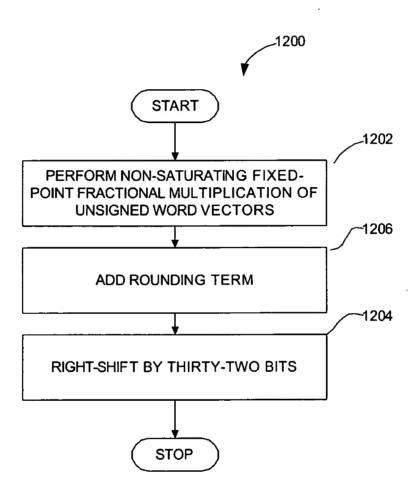


Fig. 12



Sheet 13 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

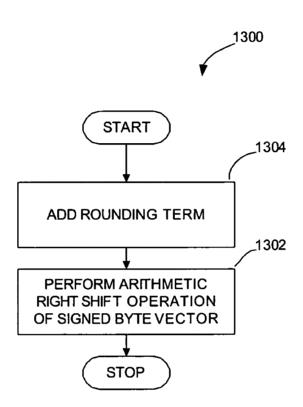


Fig. 13



Sheet 14 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

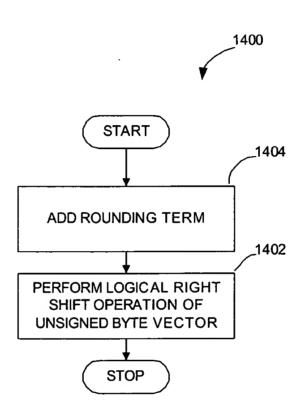


Fig. 14



Sheet 15 of 16

Title: Rounding Operations in Computer

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven

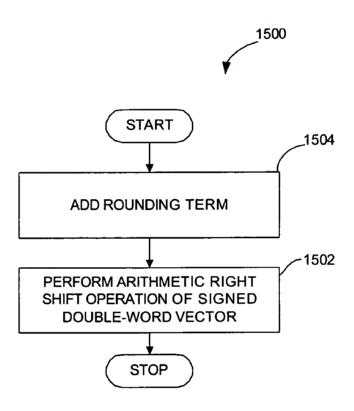


Fig. 15



Sheet 16 of 16

Title: Rounding Operations in Computer_

Processor

Inventors: Frans W. Sijstermans

and Jos van Eijndhoven Atty. Docket No.: 22300-05725



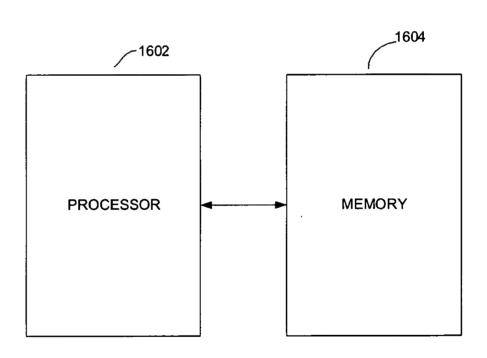


Fig. 16